

18/030194

13 Rec'd PCT/PTO 04 MAR 1993

PATENT
TESSERA 3.3-001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: :
Igor Y. Khandros, et al. :
Serial No. : Group Art Unit
Filed: concurrently herewith : Examiner
For: SEMICONDUCTOR CHIP ASSEMBLIES :
METHODS OF MAKING SAME AND :
COMPONENTS FOR SAME :
March 4, 1993

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

Sir:

"Express Mail" Mailing Label Number TB 153522490 US

Date of Deposit March 4, 1993

I hereby certify that this paper and/or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C., 20231.

Chris Lucifin